

**IEEE POWER ELECTRONICS SOCIETY ELECTION**  
**For Election of Members to the Administrative Committee**  
**For a Three-Year Term Ending 31 December 2019**



**SIBYLLE DIECKERHOFF** (M'05) Following a professional training as communication electronics technician, Sibylle Dieckerhoff completed her study of electrical engineering at RWTH Aachen, Germany with the diploma degree in 1997. She was a research assistant at the Institute for Power Electronics and Electrical Drives, RWTH Aachen, and then a PhD student at DaimlerChrysler Research and Technology, Berlin. In 2003, she received her PhD degree with honors from RWTH Aachen for a thesis on grid converters in electric traction applications. From 2003-2004, she worked as a development engineer for Siemens AG, Transportation Systems. In 2005, she became a senior scientist at the Research Center Microperipheral Technologies, Technical University of Berlin, where she did research on new packaging technologies and reliability of power electronic devices. She continued this line of research as a professor at Beuth University of Applied Sciences, Berlin, starting in 2008. Since 2010, Sibylle Dieckerhoff is a Professor for Power Electronics at the Technical University of Berlin. Her research interests include converters applying WBG devices, specifically GaN, reliability of power electronics, and grid converters in electrical grids with a large amount of power electronic components. Sibylle Dieckerhoff is a member of the International Scientific Committee of the EPE, and served as vice chairperson of the German ETG / VDE Steering Committee from 2014-2016.

**Statement:** Power electronics is facing many new challenges, arising from energy efficiency demands, future energy networks with distributed generation and many new applications as e-mobility or electric aircraft. These growing applications together with the evolving new power semiconductor devices are currently the driving forces in power electronics research and development, stimulating ideas and demanding new technological solutions. IEEE and the PELS society are leading associations in connecting researchers and the exchange of ideas worldwide, guaranteeing high quality in research by their established rigorous peer review processes, and in this way playing an important role in advancing technology. In our increasingly connected world, the internationality lived in the IEEE can be considered as an example and should be a guideline also in the future.



**ALEX HUANG** (S'91-M'91-SM'97-F'07) received his B.Sc. degree from Zhejiang University, China in 1983 and his M.Sc. degree from Chengdu Institute of Radio Engineering, China in 1986, both in electrical engineering. He received his Ph.D. from Cambridge University, UK in 1992. From 1994 to 2004, he was a professor at CPES, Virginia Tech. Since 2004, he has been with North Carolina State University and is currently the Progress Energy Distinguished Professor of Electrical and Computer Engineering. He established the NSF FREEDM Systems Engineering Research Center (ERC) in 2008 and the DOE WBG power electronics manufacturing institute in 2014. He has mentored and graduated more than 70 Ph.D. and master students, and published more than 450 papers in journals and conference proceedings, and holds 23 US patents. Dr. Huang's research areas are power semiconductor devices and power electronics.

**Statement:** This is a pivotal time for power electronics community. On one hand, the shifting towards more and more renewable electric energy and distributed generation demands for more power electronics systems, and on the other hand breakthrough technologies such as the WBG power semiconductor are making power electronics system more efficient, compact and cost less. So there is an increasing role that Power Electronics can play in the ever more technology driven society. For example, power electronics technology could transform the electric distribution grid completely into an actively managed and controlled energy infrastructure in the next 30 years. As a Member-At-Large, I would like to advocate for a stronger and more active role of PELS in shaping the future of the national critical infrastructure. In other words, I like to promote a broader agenda for the IEEE PELS that can adequately reflect the growing role and impact of power electronics technology.



**DAN KINZER** (AM'91-M'10) is COO, CTO, and Co-founder of Navitas Semiconductor, a young company focused on advanced GaN power semiconductor devices and circuits. For over 25 years, Dan has led R&D at leading power semiconductor companies at the VP level or higher. His experience includes developing advanced power device and IC platforms, wide bandgap GaN and SiC device design, IC and power device fabrication processes, advanced IC design, semiconductor package development and assembly processes, and design of electronic systems. Before Co-founding Navitas, Dan served as R&D Engineer, Director of Power IC Development, VP R&D, VP Advanced Product Development, and Chief Technologist at International Rectifier, and SVP Product & Technology Development & CTO at Fairchild Semiconductor. He has served as Technical and General Chairman of the International Symposium on Power Semiconductor Devices and ICs. He authored or co-authored multiple articles, peer-reviewed technical papers, and invited talks. Dan holds about 100 US patents, and a BSE degree in Engineering Physics from Princeton University.

**Statement:** PELS should continue to pursue the advancement of technology in power electronics through its ever-increasing slate of journals, publications, conference, tutorials, webinars, and other communication and education forms. As the premier technical society driving this advancement, the social responsibility is very high to move the field forward to the benefit of all. Activities should be

guided toward the transformation of the power electronics field through the application of advanced new semiconductors, passives, topologies, control methods, and system design. My nearly 40 years of leading power semiconductor and power electronics advances in responsible positions in industry and passion for energy efficiency in real applications drive me to want to help the society move forward to promote a strong global push in these areas as well. My background and current responsibilities will enable me to support and move forward the emerging technology, wide band gap, and reliability initiatives.



**TSORNG-JUU LIANG** (S'91-M'95-SM'10-F'16) received his MS and PhD degrees in Electrical Engineering from the University of Missouri, Columbia, USA, in 1990 and 1993, respectively. He is now the Distinguished Professor of National Cheng Kung University, Taiwan. Currently, he serves as Associate Dean at College of Electrical Engineering and Computer Science and the Director of Green Energy Electronics Research Center, NCKU. Dr. Liang had been the Technical Committee Chair TC6 of IEEE Power Electronics Society, and the BoD of Catcher Technology, Compucase Enterprise, EpiLED, and Leadtrend Technology.

Dr. Liang is the Associate Editor of *IEEE Trans. on Power Electronics*, and *Journal of Emerging and Selected Topics in Power Electronics*. Presently, he is the Chair of IEEE Tainan Section, International Editorial Advisory Board of *IEEJ IAS Journal of Industry Applications*, editor of *Journal of Power Electronics*, BoG of Taiwan Power Electronics Association, and BoG of The Chinese Institute of Electrical Engineering, Kaohsiung. In 2015, he was awarded with Outstanding Professor from Chinese Institute of Engineering. He had been the Distinguished Lecturer of IEEE CASS, 2014-2015. He served as the Program Chair and General Co-chair of IEEE International Future Energy Electronics Conference in 2013 and 2015, respectively, and the General Chair of IFEEEC2017-ECCE Asia.

Dr. Liang's research interests include high efficiency power converters, high efficiency lighting systems, and renewable energy conversion. He has published 71 journals, more than 170 conference papers, and 32 patents. In 2016, IEEE recognized Dr. Liang as IEEE Fellow for contributions to power conversion for lighting and sustainable energy.

**Statement:** IEEE Power Electronics Society (PELS) is going through very rapid and profound challenge. It is necessary for PEELS to encourage ideas of new activities and workshops for young professionals. Being a member of PEELS for more than 20 years, and having served as the Chair of IEEE Tainan Section and Technical Committee Chair of PEELS High Performance and Emerging Technologies, I will focus on the following areas if I am elected as the member of Power Electronics Society Administrative Committee (AdCom):

1. Local activities are very important in encouraging PEELS members to participate in PEELS activities. Getting two or more chapters to organize cross-chapter workshops are good opportunities in keeping chapters active. 2. Enhance the linking between PEELS technical committees and PEELS co-sponsored flagship conferences. 3. Create industry oriented Special Topics in PEELS co-sponsored flagship conferences to attract industry participants, providing a platform to link academic researches and industry developments. 4. Young members including students are certainly the future of Power Electronics Society. Hence, organizing Power Electronics Society activities for young members in various levels of PEELS activities are essential for continuity operation of Power Electronics Society.



**MARCO LISERRE** (S'00-M'02-SM'07-F'13) received the MSc and PhD degree in Electrical Engineering from the Bari Polytechnic, respectively in 1998 and 2002. He has been Associate Professor at Bari Polytechnic and Professor in reliable power electronics at Aalborg University (Denmark). He is currently Full Professor and he holds the Chair of Power Electronics at Christian-Albrechts-University of Kiel (Germany). He has published over 280 technical papers (more than 70 of them in international peer-reviewed journals) and a book. These works have received more than 17000 citations. Marco Liserre is listed in ISI Thomson report "The world's most influential scientific minds" from 2014.

He has been awarded with an ERC Consolidator Grant for the project "The Highly Efficient And Reliable smart Transformer (HEART), a new Heart for the Electric Distribution System".

He is member of IAS, PEELS, PES and IES. He has been serving all these societies in different capacities. In PEELS he is Associate Editor of the *IEEE Transactions on Power Electronics* and *IEEE Journal of Emerging and Selected Topics in Power Electronics*. He has received the IES 2009 Early Career Award, the IES 2011 Anthony J. Hornfeck Service Award, the 2014 Dr. Bimal Bose Energy Systems Award, the 2011 Industrial Electronics Magazine best paper award and the Third Prize paper award by the Industrial Power Converter Committee at ECCE 2012, 2012. In 2013 he has been elevated to the IEEE Fellow grade with the following citation "for contributions to grid connection of renewable energy systems and industrial drives".

**Statement:** I have been active more than 15 years in power electronics and I have been serving IEEE in various volunteer roles. I would like to help further the Power Electronics Society with my expertise in organizing conferences and publications. I believe in the transparency and in the inclusiveness so that nobody shall be left outside when the main decisions have to be taken. In this sense I will be always prompt to answer to your requests of information and to help you getting involved. I hope that I could deserve representing all of you.



**YAN-FEI LIU** (S'91-M'95-SM'97-F'13) received his Bachelor and Master degree from the Department of Electrical Engineering from Zhejiang University, China, in 1984 and 1987 and PhD degree from the Department of Electrical and Computer Engineering, Queen's University, Kingston, ON, Canada, in 1994.

From February 1994 to July 1999, he worked at the Advanced Power System Division of Nortel Networks, with Technical Advisor as the last position. Since 1999, he joined Queen's University. Currently, he is a Professor in the Department of Electrical and Computer Engineering. His current research interests include digital control technologies for high efficiency, fast dynamic response dc–dc switching converter and ac–dc converter with power factor correction, resonant converters and server power supplies, and LED drivers. Dr. Liu holds 20 US patents and has published more than 200 technical papers in IEEE Transactions and conferences. He is also a principal contributor for two IEEE standards. He received Premier's Research Excellence Award in 2000 in Ontario, Canada. He also received the Award of Excellence in Technology in Nortel in 1997.

Dr. Liu's recent major service to PELS are listed as following: (1) TC1 chair (Control and Modeling Core Technologies) from 2013 to 2016; (2) TC2 chair (Power Conversion Systems and Components) from 2009 to 2012; (3) General chair: ECCE 2019, Baltimore, USA; (4) General Co-Chair: ECCE 2015, Montreal, Canada; (5) Editor: *IEEE Journal of Emerging and Selected Topics of Power Electronics (IEEE JESTPE)* since 2012; (6) Guest Editor in Chief and Guest Editor for *TPEL* and *JESTPE*.

**Statement:** As a member of IEEE Power Electronics Society for more than 20 years, I have received a lot of help from our society. I have also served PELS in various capacities, such as chairs of technical committees, editor / associate editor of PELS sponsored journals, co-general chair, and TPC chair of PELS sponsored conferences. I have witnessed the significant growth of PELS in past 25 years. With the transition from the fossil energy powered society to renewable energy powered society and the extremely important role the power electronics technology will play during this transition, I believe the time is right for the even bigger growth of PELS for next 25 years with the effort of all PELS members.

As a Member at Large, I will continue to serve PELS in following ways:

- Serve as a bridge between the PELS members and PELS AdCom to bring members' need and suggestions to AdCom so that our members can be better served;
- Continue to support society's growth by promoting PELS products, such as conferences, journals to our members and new members;
- Provide more help to young / new members of PELS and help them know PELS better and their career development;
- Help the grade advancement of senior PELS members;
- Help PELS define and develop new products / services to better serve our members and expand PELS membership;
- Make myself more available to all PELS members by prompt response to all member enquiries.



**ANNETTE MUETZE** (S'03-M'04-SM'09-F'16) received the Dipl.-Ing. degree in electrical engineering from Darmstadt University of Technology, Darmstadt, Germany, and the degree in general engineering from the Ecole Centrale de Lyon, Ecully, France, both in 1999, and the Dr.Tech. degree in electrical engineering from Darmstadt University of Technology, Darmstadt, Germany, in 2004.

She is currently a full professor at Graz University of Technology, Graz, Austria, where she is the Head of the Electric Drives and Machines Institute. Prior to joining Graz, she worked as an Assistant Professor at the Electrical and Computer Engineering Department, University of Wisconsin-Madison, Madison, USA, and as an Associate Professor at the School of Engineering, University of Warwick, U.K.

Her research interests are in the field of electric drives, with a particular interest in the interaction of the different components of such systems. She has been successful both with applied research and basic research activities.

Her academic career has been characterized by significant international exposure (e.g. international work experience in the US and in Germany, France, the UK, and Austria).

The prevalent interdisciplinary nature of her activities at the interfaces of different fields within electric power, electronic, and systems engineering and her collaborations with colleagues of neighboring disciplines have allowed her to grow a significant international network both of academic researchers and industrial partners.

She has already earlier served as a PELS Member-at-Large and has been an Associate Editor for *Trans PELS* for several years.

**Statement:** In my opinion, power electronics as a recognized enabling technology, will continue to need a multifaceted approach with respect to research, education, and dissemination. On the one hand, basic research on its devices, materials, and systems, will continue to provide the foundation for the developments of the future. Their applications, on the other hand, will also need to be addressed, so as to fully exploit the potentials given by power electronics. Given my background, I am mostly interested in the interface of power

electronics and drives, an area where, in my opinion, still many improvements notably with respect to the optimal matching of the different components can be made.

While research is important, appropriate continuing educational components both for the engineers in the field, but also for end users of power electronics based systems are required to ensure the take-up of the most recent technical developments in society.

As a woman and a mother of a family myself, I also continuously seek to be a role model for female engineers, or engineers-to-be.



**EDUARD MULJADI** (S'83-M'84-SM'94-F'10) received his Ph.D. in electrical engineering from the University of Wisconsin at Madison. From 1988 to 1992, he taught at California State University at Fresno. In June 1992, he joined the National Renewable Energy Laboratory in Golden, Colorado. His current research interests are in the fields of electric machines, power electronics, and power systems in general with an emphasis on renewable energy applications. He is a member honor societies Eta Kappa Nu and Sigma Xi, a fellow of the Institute of Electrical and Electronics Engineers (IEEE), and an editor of the *IEEE Transactions on Energy Conversion*. He is involved in the activities of the IEEE Power Electronics Society (PELS), IEEE Industry Application Society (IAS), Power Electronics Society, and Power and Energy Society (PES). He is currently a member of various committees of the PELS, IAS, and PES. He has published more than 200 publications in the area of power electronics, electric machines, and power system, and is a recipient of two IEEE Prize Papers. He holds two patents in power conversion for renewable energy. He actively participates in various standard and recommended practice developments in the IEEE, IEC and the IEA.

He is currently a member of various committees of the PELS, IAS, and PES. He has published more than 200 publications in the area of power electronics, electric machines, and power system, and is a recipient of two IEEE Prize Papers. He holds two patents in power conversion for renewable energy. He actively participates in various standard and recommended practice developments in the IEEE, IEC and the IEA.

**Statement:** The use of power electronics in modern industry has been dramatically increased in the past few decades, driven by the cost reduction and a broad spectrum of industries such as communication, aerospace, transportation, and power industries, to name a few. With the latest development and availability of the wideband gap power electronic devices, advancement in control and communication systems, the opportunities to bring this technology for large scale power applications open up very widely.

I fully understand the importance that this technology can contribute especially in the area of renewable energy conversion (wind, solar, water, marine hydrokinetic), power system (both at the distribution and transmission levels), mining, transportation, and heavy machinery industries. The present large scale deployment renewable energy has added complexity to power systems planning and operation. However, presence of power electronics in the generation, transmission, and distribution levels has provided the flexibility and the fast response required to accommodate the variability nature of renewable generation.

If elected member at large for the IEEE PELS, I intend to support efforts in pushing this technology forward for large scale and high power applications.



**JOÃO O. P. PINTO** (S'98-M'01-SM'12) was born in Valparaíso, Brazil. He received the B.S. degree in Electrical Engineering from Universidade Estadual Paulista, Ilha Solteira, Brazil, in 1990, the M.S. degree from Universidade Federal de Uberlândia, Uberlândia, Brazil, in 1993, and the Ph.D. degree from The University of Tennessee, Knoxville, in 2001. He is a Faculty Member at the Universidade Federal de Mato Grosso do Sul (UFMS), Campo Grande, Brazil. His research interests include signal processing and artificial intelligence applications to power electronics, machine drives and power systems, among others.

He is the founder and coordinator of the BATLAB, Artificial Intelligence Applications, Power Electronics and Drives, and Digital Systems Laboratory. He served as Editor-in-Chief of the *Brazilian Power Electronics Transactions*, and also was the President of the Brazilian Power Electronics Society.

During four years he was the graduate committee chair at UFMS, and from August 2008 until December 2011 he was the Mato Grosso do Sul State Undersecretary for Research and Development Issues. Currently he is the Dean of the Engineering College.

He is two times first award winner of IEEE – IFEC International Future Energy Challenge. He was the IEEE-IFEC General Chair for the 2011 edition and member of the steering committee for the IEEE-IFEC 2013. He served PELS AdCom as Member at Large from 2013-2015, and he also is IEEE Region 9 PELS Liaison since 2013 and Regional Chair since 2015.

He is very active in education and research working very close to industry in Brazil.

**Statement:** Since the end of 20th century, Power Electronics is gaining more and more importance and visibility. In one hand, this is the unique area of electrical engineering that brings together the best of all areas, such as power systems, signal processing, control. On the other hand, this is the area that makes energy harvesting possible, which is extremely important, due the current worldwide concern about environmental protection, assuring equilibrium in the natural resources use, resulting in a habitable world for the future generations. Despite its importance, power electronics education is still lacking further development in methods to, along with job market attractiveness, increase the number of students interested in becoming Power Electronics Engineers. Especially considering the main current challenge that Education is facing, which is to reach high interactivity during the learning process.

Although PELS has broader missions than the contributions to improve power electronics education, my main goal, as member at large of the PELS AdCom, is to expend extra efforts focusing in the education of Power Electronics, targeting to attract more and more students to this important area.



**NADIA M. L. TAN** (GSM'07-M'10) received the B. Eng. (Hons.) degree from University of Sheffield, United Kingdom, in 2002, the M. Eng. degree from Universiti Tenaga Nasional, Malaysia, in 2007, and the Ph.D. degree from Tokyo Institute of Technology, Japan, in 2010, all in electrical engineering.

Since October 2010, she has been a senior lecturer in the Department of Electrical Power Engineering, Universiti Tenaga Nasional (UNITEN). Her research interests include power conversion and control for energy storage and renewable energy systems. Since 2014, she is an associate researcher with the Institute of Energy Policy and Research (IEPR) in UNITEN, leading the energy sector team in preparing the National Greenhouse Gas Inventory, a part of the Biennial Update Report, which was submitted to the United Nations Framework Convention on Climate Change (UNFCCC) in 2015. She visits Tokyo Institute of Technology annually, between years 2011 and 2014, as a visiting researcher. From July to September, 2016, she was an adjunct associate professor at Tokyo Institute of Technology.

Dr. Tan served IEEE Malaysia PELS Chapter as an executive committee member in 2015. She was also an organizing committee for the 2015 IEEE Energy Conversion Conference (CENCON). Furthermore, she was an active volunteer for IEEE Malaysia Section-Women in Engineering Affinity Group as its executive committee member and secretary in 2014 and 2015, respectively. She is also a graduate member of the Institution of Engineers Malaysia, a member of the Institution of Engineering and Technology (IET), and a Chartered Engineer, registered with the Engineering Council.

**Statement:** Power electronics technology is one of the key enablers as the world progressively embraces sustainable energy. The proliferation of power electronics applications will continue to create opportunities for research and development in this field. Our society has been relevant and shall continue to be so, for our members. Therefore, I believe that the move by the society's administration officers in initiating the long-range planning and strategy, new workshops and activities will persevere to best serve our members from diverse technical, cultural, and geographical background. In moving forward, it is also important to get young professionals on board to ensure continuity and smooth transition in leadership in the next 10 to 15 years.

It is indeed a great honor to be nominated as a candidate for member-at-large. I would like to be elected as a member-at-large so that I can have a first-hand experience in supporting our society's purpose, goals and strategic plans. I intend to make the most of my experiences to promote power electronics to engineers, utilities, and students, which would translate into increasing and retaining memberships for our society. I am also keen to potentially oversee that one of the ECCE conferences will be successfully held in non-OECD countries in the next 15 years, as I believe in the potential for growth in power electronics research and development, and applications in these countries.



**JIN WANG** (S'05-GSM'09-M'10-SM'15) received his B.S. degree from Xi'an Jiaotong University, in 1998, M.S. degree from Wuhan University, in 2001, and Ph.D. from Michigan State University, East Lansing, in 2005, all in electrical engineering.

From Sept., 2005 to Aug. 2007, he worked at the Ford Motor Company as a Core Power Electronics Engineer. He joined The Ohio State University (OSU) as an assistant professor in September 2007 and was promoted to associate professor in September 2013. His research interests include wide bandgap power devices and their applications, high-voltage and high-power converter/inverters, integration of renewable energy sources, and electrification of transportation.

Dr. Wang received multiple research and teaching awards including the IEEE Power Electronics Society Richard M. Bass Young Engineer Award and the National Science Foundation's CAREER Award, both in 2011; Ralph L. Boyer Award for Excellence in Undergraduate Teaching Innovation in 2012, and the Lumley Research Award in 2013, both from the College of Engineering at The Ohio State University.

Dr. Wang has over 100 peer-reviewed journal and conference publications and five patents. Dr. Wang had been an Associate Editor for *IEEE Transactions on Industry Applications* from 2008 to 2014. He initiated and served as the General Chair for the 1st IEEE Workshop on Wide Bandgap Power Devices and Applications in 2013. Currently, Dr. Wang serves as an Associate Editor for *IEEE Transactions on Power Electronics* and the guest Editor for the Special Issue on Wide Bandgap Power Electronics in *IEEE Journal of Emerging and Selected Topics in Power Electronics (J-ESTPE)*.

**Statement:** For technology development, I believe that the future of power electronics will be based on new generation of wide bandgap (WBG) switching devices. I would like to help the society to develop and maintain roadmaps of WBG technology and advocate WBG in conferences and workshops.

For education, I believe that we attract more young students by showing what one can do with power electronics in tangible and exciting ways including student competitions and industry demonstrations at universities.

I initiated the IEEE Workshop on Wide Bandgap Power Devices and Applications (WiPDA) and chaired the inaugural workshop in 2013. I am a founding member of the Technical Committee (TC) on High Performance and Emerging Technologies and has served as the award chair and secretary for the TC in the last four years. I also have been serving as an organizing committee member for APEC, ECCE and other leading power electronics conferences. On the education front, I have been a key organizer of the IEEE Future Energy Challenge in the last few years. I hosted the competition as a Topic Chair in 2013, and served as the Co-Chair and General Chair for 2015 and 2016, respectively.

With my experience and devotion to the power electronics research, teaching and service, I hope that I can bring more contributions to PELS by becoming a Member-at-Large.



**MARK DEHONG XU** (M'94-SM'10-F'13) received Ph.D. degree from Zhejiang University, China in 1989. He became an assistant professor in 1989, an associate professor in 1991, and a professor in 1996 in College of Electrical Engineering of Zhejiang University. He was a visiting professor in University of Tokyo in 1995, in Virginia Tech in 2000, and in ETH in 2006.

His current research interests include power electronics topology and control, and its applications to energy efficiency and renewable energy such as in PV and wind inverters, Fuel Cell systems, and power supplies for the data center. He authored six books and 160 IEEE Journal or Conference papers. He owns more than 30 Chinese patents and 3 US patents. He received four IEEE journal or conference prize paper awards.

He is an associate editor of *IEEE Transactions on Power Electronics* and *IEEE Transactions on Sustainable Energy*. He was guest editors of several special issues on IEEE transactions. He was the General Chair of IEEE International Symposium on Industrial Electronics (ISIE2012, Hangzhou), IEEE International Symposium on Power Electronics for Distributed Generation Systems (PEDG2013, Arkansas), IEEE Power Electronics and Applications (PEAC2014, Shanghai), and International Future Energy Challenge Competition (IFEC2015).

He is IEEE Fellow in 2013. He is IEEE PELS Distinguish Lecturer in 2015-2016. He received IEEE PELS R. D. Middlebrook Achievement Award in 2016.

**Statement:** I would like to contribute to the following efforts:

1. Power electronics is one of key technologies for addressing the climate changing and building sustainable society. Young engineers are mainstream explorers of future power electronics and also new blood to keep IEEE power electronics society to be full of growth in 21 century. We should encourage more young engineers involved in the activities, and operation of PELS society with respects to both scale and depth. To hear more voice from the younger generation, new mechanism or organizational arrangement such as young power electronics engineer committee may be needed.

2. Asia has higher growth on power electronics engineers with the increase of its economy. Besides holding ECCE Asia, PEDS, PEAC, SPEC conferences in Asia, more in-depth service to our members are needed to attract Asian engineers and support their career development. Since PELS has the strongest human resource and industrial networks on power electronics, more seminars and training programs can be organized to satisfy the requirement of Asia power electronics engineers according to the local industry development condition. Seminars and training programs can be organized with local companies, institutes or local societies who have ties with PELS. How the existing PELS precious resources can be shared by all PELS member should be seriously considered.